

JAYPEE UNIVERSITY OF INFORMATION TECHNOLOGY, WAKNAGHAT

TEST -1 EXAMINATIONS FEB 2023

B. Tech – VI Semester

COURSE CODE (CREDITS):18B1WCI735(3)

MAX. MARKS: 15

COURSE NAME: ARM Based Embedded System Design

COURSE INSTRUCTOR: Dr. Naveen Jaglan

MAX. TIME: 1 Hour

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*Note: All questions are compulsory. Carrying of mobile phone during examinations will be treated as case of unfair means. Marks are indicated against each question in square brackets.*

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Ques 1: What will be status of Carry Flag, Auxiliary Carry Flag, Overflow Flag and Parity Flag in the addition of 42H and 43H. [CO-1, 2: 2 Marks]

Ques 2: WAP to multiply the numbers B2H and 2FH. Store the result in register R0 (LSB) and R1 (MSB) of bank 0. [CO-1, 2: 3 Marks]

Ques 3: WAP to add a series of 10 numbers. The series begins from location 2000H in External RAM. Store the result at 3000H and 3001H. [CO-1, 2: 3 Marks]

Ques 4: With the help of an example explain the difference between LJMP, SJMP and AJMP instructions. [CO-1: 3 Marks]

Ques 5: WAP to copy the value 25H to all locations from 2000H to 2100H in the external RAM. [CO-1, 2: 2 Marks]

Ques 6: Write four differences between Harvard Architecture and Von Neumann Architecture? [CO-1: 2 Marks]