Ref. SDRV
SP Ghrera

JAYPEE UNIVERSITY OF INFORMATRION TECHNOLOGY, WAKNAGHAT

TEST -1 EXAMINATION- Feb 2016

B.Tech(CSE) IV Semester

COURSE CODE: 10B11CI401 **MAX. MARKS: 15**

COURSE NAME: Microprocessors and Controllers

COURSE CREDITS: 4 **MAX. TIME: 1 HR**

Note: All questions are compulsory. Carrying of mobile phone during examinations will be treated as case of unfair means.

Q.1. [3 Marks. Each question is half mark]

a) Explain the addressing of stack in 8086?

b) Explain the instruction REPZ CMPSB.

c) If segment address is ABCDh and offset address is DCBAh, find real mode address?

d) What is protected mode operation?

e) What is the difference between a LATCH and a BUFFER?

f) Explain TTL 1 and 0 levels for 8086 microprocessor.

Q.2. [2 marks] Describe the maximum mode operation of 8086 using 8288.

Q.3. [2 marks] Explain the following pinouts of 8086.

DT/R, MI/O, BHE, TEST

Q.4. [2 marks] Describe how the EFI may be used in the clock generator 8284.

Q.5. [2 marks] Explain with examples the data addressing modes for 8086 microprocessor.

Q.6. [2 marks] With the help of timing diagram illustrate the memory write operation. Also explain the role of the READY control signal.

Q.7. [2 marks] Explain the role of registers in a microprocessor. In 8086 the general purpose registers are not truly general purpose. Elaborate with example of each.